

DSG-NPS R&D Meeting Minutes

Date: May 04, 2021

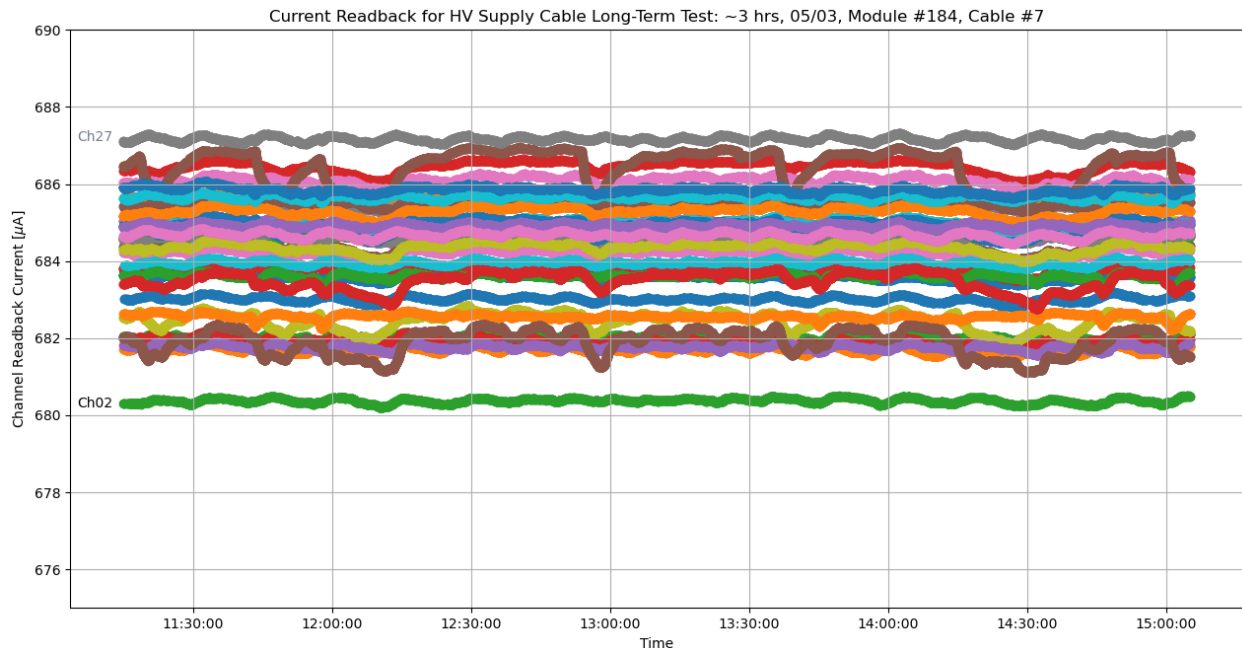
Time: 11:00AM – 12:15 PM

Attendees: Mary Ann Antonioli, Peter Bonneau, Aaron Brown, Brian Eng, George Jacobs, Mindy Leffel, Tyler Lemon, and Amrit Yegneswaran

1. HV supply cable testing

Peter Bonneau, Aaron Brown, Brian Eng, George Jacobs, Mindy Leffel, and Marc McMullen

1. Reviewed plot of load, long-term test of cable #7 in module #184
 - The readback current for all channels is $\sim 7\mu\text{A}$ above the expected current based on the measured resistor values for each channel
 - Will conduct long-term test of cable #7 using a different module in an effort to rule out the module as the cause of the higher than expected currents
2. Moving forward, the long-term test will be conducted on all 36 channels of each cable and a switching test will be conducted for a random selection of ~ 3 channels for each cable



Plot of three-hour stability test for cable #7 in module #184; all channels have a higher than expected current with channels #2 and #27 having the lowest and highest, respectively

2. Hardware interlock system development

Mary Ann Antonioli, Peter Bonneau, and Aaron Brown

1. Reviewed timeline for completion of hardware interlock system development and testing
 - Timeline will be revised to separate task items with multiple tasks
 - A time contingency of ~ 1.5 weeks will be added to each task item